Hardware/Software Co-Design of Embedded Systems
(ESE 566)
Fall 2003
Guidelines for Report 3

1 Introduction

The goal of the third report is to familiarize you with modern methodologies and techniques for hardware/software co-synthesis. You can focus your reading on co-synthesis for processor-coprocessor architectures, distributed systems, embedded systems, real-time systems, and low power systems. The list of recommended papers is posted on the course www page accessible through my personal page (www.ece.sunysb.edu/ adoboli). The report should discuss the 7 papers presented in class and *at least* 3 other papers from the list.

The report can have the same section structure as this handout. Nevertheless, you are encouraged to organize your report in any way you consider meaningful. As a rule of thumb, the report should be around 8 pages long using normal spacing and size 10 characters. Following is a possible structure of your report:

- Definition of the goals of your report.
- Brief introduction into hardware/software co-synthesis. Definition of the hardware/software co-synthesis tasks. Advantages of co-synthesis.
- Critical discussion of the 5 approaches.
- Challenges for co-synthesis. opportunities for future research.
- Conclusions

Summarize the experience and knowledge you gathered by preparing this report. Grading will be based on your report (50%), and the report review (50%). Grading will considering the breadth and depth of your work, as well as understanding of the concepts. *I strongly encourage creative thinking which leads towards identifying new, relevant research problems.*

Please do not replicate in you reports information presented in a paper! This refers to figures, too!