

Table 1: N-Well

Rule	Description	Lambda
1.1	Minimum width	12
1.2	Minimum spacing between wells at different potential	18
1.3	Minimum spacing between wells at same potential	0 or 6
1.4	Minimum spacing between wells of different type (if both are drawn)	ignore

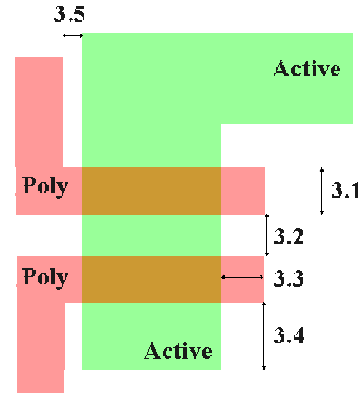


Table 3: Poly

Rule	Description	Lambda
3.1	Minimum width	2
3.2	Minimum spacing	3
3.2.a	Minimum spacing over active	3
3.3	Minimum gate extension of active	2
3.4	Minimum active extension of poly	3
3.5	Minimum field poly to active	1

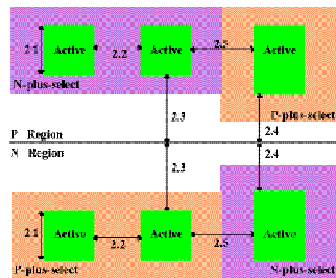
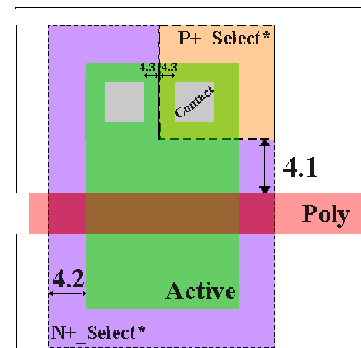


Table 2: Active

Rule	Description	Lambda
2.1	Minimum width	3
2.2	Minimum spacing	3
2.3	Source/drain active to well edge	6
2.4	Substrate/well contact active to well edge	3
2.5	Minimum spacing between active of different implant	0 or 4



!!The same rules apply with N+ Select and P+ Select reversed.

Table 4: Select

Rule	Description	Lambda
4.1	Minimum select spacing to channel of transistor to ensure adequate source/drain width	3
4.2	Minimum select overlap of active	2
4.3	Minimum select overlap of contact	1
4.4	Minimum select width and spacing (Note: P-select and N-select may be coincident, but must not overlap)	2

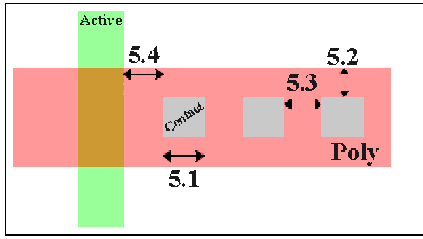


Table 5: Contact to Poly

Rule	Description	Lambda
5.1	Exact contact size	2 x 2
5.2	Minimum poly overlap	1.5
5.3	Minimum contact spacing	3
5.4	Minimum spacing to gate of transistor	2

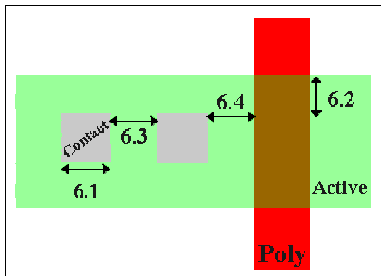


Table 6: Contact to Active

Rule	Description	Lambda
6.1	Exact contact size	2 x 2
6.2	Minimum active overlap	1.5
6.3	Minimum contact spacing	3
6.4	Minimum spacing to gate of transistor	2

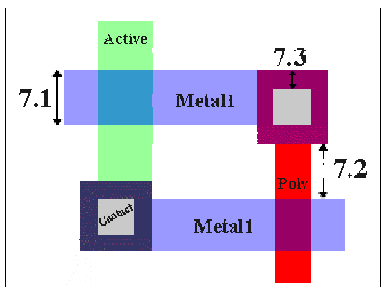


Table 7: Metall1

Rule	Description	Lambda
7.1	Minimum width	3
7.2	Minimum spacing	3
7.3	Minimum overlap of any contact	1

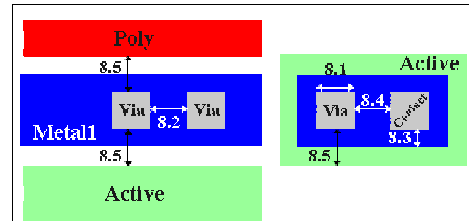


Table 8: Via1

Rule	Description	Lambda
8.1	Exact contact size	2 x 2
8.2	Minimum via1 spacing	3
8.3	Minimum overlap by metall1	1

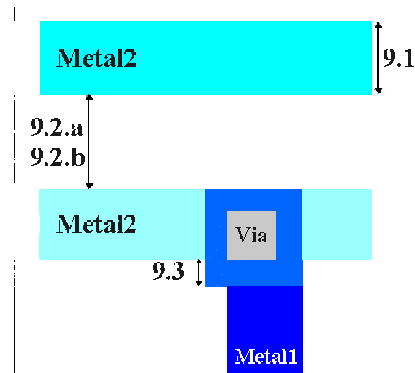


Table 9: Metal2

Rule	Description	Lambda
9.1	Minimum width	3
9.2	Minimum spacing	3
9.3	Minimum overlap of via1	1

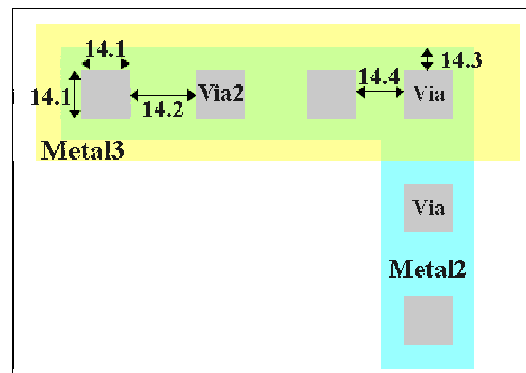


Table 10: Via2

Rule	Description	Lambda
14.1	Exact size	2 x 2
14.2	Minimum spacing	3
14.3	Minimum overlap by metal2	1

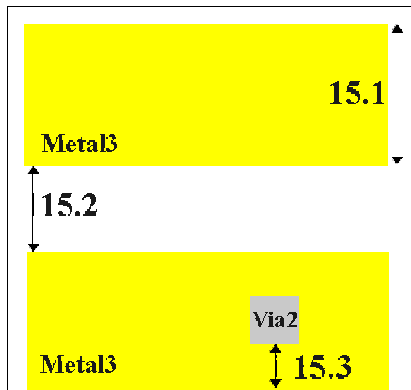
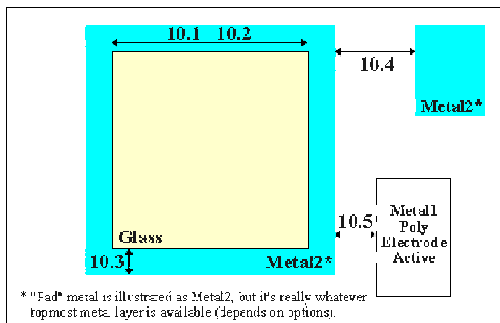


Table 11: Metal3

Rule	Description	Lambda
15.1	Minimum Width	5
15.2	Minimum spacing	3
15.3	Minimum overlap of via2	2



* "Pad" metal is illustrated as Metal2, but it's really whatever topmost metal layer is available (depends on options).

Table 12: Overglass

Rule	Description	Lambda
10.1	Minimum bonding pad width	100 x 100
10.2	Minimum probe pad width	75 x 75
10.3	Pad metal overlap of glass opening	6
10.4	Minimum pad spacing to unrelated metal	30
10.5	Minimum pad spacing to unrelated active, poly or poly2	15

These are roughly equivalent to the SCMOS rules with a few exceptions. You really only need to be aware of the differences if you refer to the design rule web pages on the MOSIS website.

- We use the SUBM version of the SCMOS rules.
- We allow only simple contact to poly and active, not the alternative contacts.
- We don't use tight metal spacing rules.
- Vias and contacts may be stacked. There are no interlayer constraints for contact, via1 or via2.
- Metal3 overlap of via2 is 2 lambda, not 1.