

Fundamental Limits of Cyber-Physical Security in Smart Power Grids

Yue Zhao, Andrea Goldsmith, and H. Vincent Poor

Abstract—Cyber-physical security of power systems under power injection attacks that alter generation and loads is studied. The system operator employs Phasor Measurement Units (PMUs) for detecting such attacks, while attackers devise attacks that are *unobservable* by such PMU networks. For the NP-hard problem of finding the *sparsest* unobservable attacks, it is shown that the solution has a simple form with probability one, namely, $\min(\kappa(\mathcal{G}^M), M) + 1$, where $\kappa(\mathcal{G}^M)$ is the vertex connectivity of an augmented graph, and M is the number of PMUs. The constructive proof allows one to find the entire set of the sparsest unobservable attacks in polynomial time. Furthermore, the geometric interpretation of unobservable attacks leads to a natural characterization of their potential impacts. With optimized PMU deployment, the sparsest unobservable attacks and their potential impact as functions of the number of PMUs are evaluated numerically for IEEE 30, 57, 118, 300-bus systems and Polish 2383, 2737, 3012-bus systems. It is observed that, as more PMUs are added, the maximum potential impact among all the sparsest unobservable attacks drops quickly until it reaches the minimum sparsity.

I. INTRODUCTION

Modern power networks are increasingly dependent on information technology in order to achieve higher efficiency, flexibility and adaptability. The development of more advanced sensing, communications and control capabilities for power grids enables better situational awareness and smarter control. However, security issues also arise as more complex information systems become prominent targets of cyber-physical attacks: not only can there be data attacks on measurements that disrupt situation awareness [1], but also control signals of many power grid components including generation and loads can be hijacked, directly leading to physical misbehavior of power systems [2]. Therefore, to achieve reliable and secure operation of a smart power grid, it is essential for the system operator to minimize (if not eliminate) the feasibility and impact of such cyber-physical attacks.

Recently, there has been considerable research concerning data injection attacks on sensor measurements, particularly from supervisory control and data acquisition (SCADA) systems. A common and important goal among these works

is to pursue the integrity of network *state estimation*, that is, to successfully detect the injected data attack and recover the correct system states. The feasibility of constructing false data injection attacks to pass bad data detection schemes and alter estimated system states was first shown in [1]. There, a natural question arises as to how to find the *sparsest unobservable* data injection attack, as sparsity is used to model the complexity of an attack, as well as the resources needed for an attacker to implement it. However, finding such an *optimal attack* requires solving an NP-hard l_0 minimization problem. While efficiently finding the sparsest unobservable attacks in general remains an open problem, many interesting solutions under special problem settings have been developed (see, e.g. [3] and [4]). Furthermore, as PMUs become increasingly deployed in power systems, network situational awareness for grid operators is significantly improved compared to using legacy SCADA systems only. However, the high installation costs of PMUs still prohibit large-scale deployment. Thus, the problem of how to economically deploy PMUs to best facilitate the state estimator to detect data injection attacks becomes an interesting problem that many studies have addressed (see, e.g. [5] and [6] among others.)

Compared to data attacks that target state estimators, cyber-physical attacks that directly disrupt power network physical processes can have a much faster (and often stronger) impact on power grids. In addition to physical attacks implemented by hacking control signals or directly intruding upon grid components, several types of load altering attacks have been shown to be practically implementable via Internet-based message attacks [2]. Topological attacks are another type of physical attack which have been considered in [7]. Furthermore, there have been studies of dynamic power injection attacks [8], [9].

In this paper, we investigate a general type of cyber-physical attacks in power systems, namely, *power injection attacks* that alter generation and loads in the network. We consider a grid operator that employs PMUs to (partially) monitor the network for detecting power injection attacks. Since power injection attacks disrupt the power system states immediately, the timeliness of PMU measurement feedback is essential. We study the open l_0 minimization problem of finding the sparsest unobservable attacks given any set of PMU locations. We prove that this in general NP-hard problem has a simple solution with probability one, namely, the sparsity of the optimal solution is $\min(\kappa(\mathcal{G}^M), M) + 1$, where $\kappa(\mathcal{G}^M)$ is the vertex connectivity of an augmented graph of the original power network, and M is the number of PMUs. Furthermore, the geometric interpretation of these sparsest unobservable attacks leads to a natural characteri-

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zation of their potential impact. Accordingly, among all the sparsest unobservable attacks, an attacker can easily find the one with the greatest potential impact. Finally, for all possible numbers of PMUs with optimized placement, we evaluate the sparsest unobservable attacks in terms of their sparsity and potential impact in IEEE 30, 57, 118, 300-bus and Polish 2383, 2737, 3012-bus systems.

The remainder of the paper is organized as follows. In Section II, models of the power network, power injection attacks and PMUs are established. We then formulate the minimum sparsity problem of unobservable attacks. In Section III, we prove that the minimum sparsity of unobservable attacks can be found in polynomial time with probability one. The potential impact of unobservable attacks is characterized based on a geometric interpretation. In Section IV, numerical evaluation of the sparsest unobservable attacks in IEEE benchmark test cases and Polish power systems are provided. Conclusions are drawn in Section V. Due to space limitations, proof details (except for Theorem 2) are omitted here, and can be found in [10].

II. PROBLEM FORMULATION

A. Power network model

We consider a power network with N buses, and denote the set of buses and the set of transmission lines by $\mathcal{N} = \{1, \dots, N\}$ and $\mathcal{L} = \{1, \dots, L\}$ respectively. For a line $l \in \mathcal{L}$ that connects buses n and m , denote its reactance by x_l as well as x_{nm} , and define its *incidence vector* \mathbf{m}_l as follows:

$$\mathbf{m}_l(i) = \begin{cases} 1, & \text{if } i = n, \\ -1, & \text{if } i = m, \\ 0, & \text{otherwise.} \end{cases}$$

Based on the power network topology and line reactances, we construct a weighted graph $\mathcal{G} = \{\mathcal{N}, \mathcal{L}, \mathbf{w}\}$ where the edge weight $w_l \triangleq \frac{1}{x_l}, \forall l = 1, \dots, L$. We employ the DC power flow model in this paper, and the real power injections $\mathbf{P} \in \mathbb{R}^N$ and the voltage phase angles $\boldsymbol{\theta} \in \mathbb{R}^N$ satisfy $\mathbf{P} = \mathbf{B}\boldsymbol{\theta}$, where $\mathbf{B} = \sum_{l=1}^L \frac{1}{x_l} \mathbf{m}_l \mathbf{m}_l^T \in \mathbb{R}^{N \times N}$ is the *Laplacian* of the weighted graph \mathcal{G} . Furthermore, the power flow on line l from bus n to bus m equals $P_{nm} = \frac{1}{x_{nm}}(\theta_n - \theta_m)$.

We consider attackers inflicting power injection attacks that alter the generation and loads in the power network. We denote the power injections under normal conditions by \mathbf{P} , and denote a power injection attack by $\Delta\mathbf{P} \in \mathbb{R}^N$. Thus the post-attack power injections are $\mathbf{P} + \Delta\mathbf{P}$.

B. Sensor model and unobservable attacks

We consider the use of PMUs by the system operator for monitoring the power network in order to detect power injection attacks. With PMUs installed at the buses, we consider the following two different sensor models:

- 1) A PMU securely measures the voltage phasor of the bus at which it is installed.
- 2) A PMU securely measures the voltage phasor of the bus at which it is installed, as well as the current

phasors on all the lines connected to this bus¹.

We denote the set of buses with PMUs by $\mathcal{M} (\subseteq \mathcal{N})$, and let $M \triangleq |\mathcal{M}|$ be the total number of PMUs, where $|\cdot|$ denotes the cardinality of a set. We say that a power injection attack $\Delta\mathbf{P}$ is *unobservable* if it leads to *zero* changes in all the quantities measured by the PMUs. With the first PMU model described above, we have the following definition:

Definition 1 (unobservability condition): An attack $\Delta\mathbf{P}$ is unobservable if and only if

$$\exists \Delta\boldsymbol{\theta}, \text{ such that } \Delta\mathbf{P} = \mathbf{B}\Delta\boldsymbol{\theta} \text{ and } \Delta\boldsymbol{\theta}_{\mathcal{M}} = \mathbf{0}, \quad (1)$$

where $\Delta\boldsymbol{\theta}_{\mathcal{M}}$ denotes the $M \times 1$ sub-vector of $\Delta\boldsymbol{\theta}$ obtained by keeping its M entries whose indices are in \mathcal{M} .

With the second PMU model described above, for any bus $n \in \mathcal{N}$, it is immediate to verify that the following three conditions are equivalent:

- 1) There are no changes of the voltage phasor at n and of the current phasors on all the lines connected to n .
- 2) There are no changes of the voltage phasor at n and of the power flows on all the lines connected to n .
- 3) $\forall n' \in N[n]$, there is no change of the voltage phasor at n' , where $N[n]$ is the closed neighborhood of n which includes n and its neighboring buses $N(n)$.

Thus, for forming unobservable attacks, the following two situations are equivalent to the attacker:

- The system operator monitors the set of buses \mathcal{M} with the second PMU model;
- The system operator monitors the set of buses $N[\mathcal{M}]$ with the first PMU model,

where $N[\mathcal{M}]$ is the closed neighborhood of \mathcal{M} which includes all the buses in \mathcal{M} and their neighboring buses $N(\mathcal{M})$. Thus, the unobservability condition with the second PMU model is obtained by replacing \mathcal{M} with $N[\mathcal{M}]$ in (1). Without loss of generality (WLOG), we employ the first PMU model in the following analysis, and all the results can be directly translated to the second PMU model.

C. Sparsest unobservable attacks

In forming an unobservable attack, an attacker generally has two objectives: minimize execution complexity and maximize its impact on the grid. Note that these two objectives can be competing interests that are not simultaneously achievable. For an attack vector $\Delta\mathbf{P}$, we use its zero norm $\|\Delta\mathbf{P}\|_0$ to model its complexity. For minimizing attack complexity, an attacker is interested in finding the sparsest attacks that satisfy the unobservability condition (1):

$$\begin{aligned} \min_{\Delta\boldsymbol{\theta}} \|\Delta\mathbf{P}\|_0 \\ \text{s.t. } \Delta\mathbf{P} = \mathbf{B}\Delta\boldsymbol{\theta}, \Delta\boldsymbol{\theta}_{\mathcal{M}} = \mathbf{0}, \Delta\boldsymbol{\theta} \neq \mathbf{0}. \end{aligned} \quad (2)$$

Equivalently, a more compact form of (2) is as follows:

$$(2) \Leftrightarrow \min_{\Delta\boldsymbol{\theta}_{\mathcal{M}^c} \neq \mathbf{0}} \|\mathbf{B}_{\mathcal{N}, \mathcal{M}^c} \Delta\boldsymbol{\theta}_{\mathcal{M}^c}\|_0, \quad (3)$$

¹In practice, the second PMU measurement model is achieved by installing PMUs on all the lines connected to a bus.

Because of the non-convexity of the l_0 norm, problem (3) is in general NP-hard. Instead of applying existing heuristics (e.g., l_1 relaxation), in Section III, we will solve (3) by analyzing the network topology and the structure of the Laplacian matrix \mathbf{B} . We will then characterize the potential impact associated with unobservable attacks.

III. SOLVING FOR THE SPARSEST UNOBSERVABLE ATTACKS

In this section, we study the problem of finding the sparsest unobservable attacks given any set of PMUs \mathcal{M} (cf. (3)). We first show an important role of the *vertex connectivity* of the grid in lower bounding the optimum of (3). We then derive an upper bound of (3). By further exploiting the geometric insights behind the upper bound, we close the gap between the lower and upper bounds, and provide a complete solution to the minimum sparsity problem (3). Finally, we characterize the potential impact of unobservable attacks based on their geometric interpretations.

A. The role of vertex connectivity in lower bounding the sparsity of unobservable attacks

We first make the following definitions:

Definition 2 (Vertex cut): A vertex cut of a connected graph \mathcal{G} is a set of vertices whose removal renders \mathcal{G} disconnected.

Definition 3 (Vertex connectivity): The vertex connectivity of a graph \mathcal{G} , denoted by $\kappa(\mathcal{G})$, is the size of the minimum vertex cut of \mathcal{G} , i.e., it is the minimum number of vertices that need to be removed to disconnect the remaining graph.

We now state the following theorem.

Theorem 1: For a connected power grid $\mathcal{G} = \{\mathcal{N}, \mathcal{L}, \mathbf{w}\}$, assume that the line reactances x_l ($l \in \mathcal{L}$) are independent continuous random variables strictly bounded away from zero from below. $\forall M \leq \kappa(\mathcal{G})$, for any set of buses $\mathcal{M} \subset \mathcal{N}$, $|\mathcal{M}| = M$, in order to have $\Delta\boldsymbol{\theta}_{\mathcal{M}} = \mathbf{0}$, one of the following must be true with probability one:

- There is no power injection attack at any bus in the grid, i.e., $\Delta\mathbf{P} = \mathbf{0}$; or
- There must be at least $M+1$ buses with non-zero power injections from an attack, i.e., $\|\Delta\mathbf{P}\|_0 \geq M+1$.

Theorem 1 provides a lower bound on the optimum of (3) which holds with probability one, i.e., no matter which set of M buses' phase angles are monitored by PMUs,

- if $M \leq \kappa(\mathcal{G})$, there must be at least $M+1$ power injections in any non-zero unobservable attack;
- if $M > \kappa(\mathcal{G})$, there must be at least $\kappa(\mathcal{G})+1$ power injections in any non-zero unobservable attack.

In sum, the minimum sparsity of unobservable attacks is lower bounded almost surely by $\min(\kappa(\mathcal{G}), M)+1$. We will see in the following that while this lower bound is not always tight, a modification of it will render the optimum of (3).

B. An upper bound on the minimum sparsity of unobservable attacks

To derive an upper bound on the minimum sparsity of unobservable attacks, we exploit the fact that solving (3) is

equivalent to finding the sparsest non-zero vector in the *range space* of $\mathbf{B}_{\mathcal{N}\mathcal{M}^c}$. We have the following theorem:

Theorem 2: For a connected grid, the optimum of (3) is upper bounded by $|N(\mathcal{M}^c)|+1(\leq M+1), \forall 1 \leq M \leq N-1$.

Proof: By re-indexing the buses, WLOG, i) let $\mathcal{M}^c = \{1, 2, \dots, N-M\}$, and ii) let $\mathbf{B}_{\mathcal{N}\mathcal{M}^c}$ have the following partition as depicted in Figure 1(a):

- 1) The top submatrix $\mathbf{B}_{\mathcal{M}^c\mathcal{M}^c}$ is an $(N-M) \times (N-M)$ full-rank matrix.
- 2) The middle submatrix (which will be shown to be $\mathbf{B}_{N(\mathcal{M}^c)\mathcal{M}^c}$) consists of all the remaining rows each of which has at least one *non-zero* entry.
- 3) The bottom submatrix is an *all-zero* matrix.

In particular, from the definition of the Laplacian, the middle submatrix of $\mathbf{B}_{\mathcal{N}\mathcal{M}^c}$ as described above is exactly $\mathbf{B}_{N(\mathcal{M}^c)\mathcal{M}^c}$ as its row indices correspond to those buses not in \mathcal{M}^c but connected to at least one bus in \mathcal{M}^c . Note that the middle and the bottom sub-matrices can be degenerate. Now, we let

$$\Delta\boldsymbol{\theta}_{\mathcal{M}^c} = \mathbf{B}_{\mathcal{M}^c\mathcal{M}^c}^{-1} \mathbf{e}_1, \quad (4)$$

where $\mathbf{e}_1 \in \mathbb{R}^{(N-M) \times 1}$ is the natural basis $[1, 0, \dots, 0]^T$. Then, we construct an attack vector $\Delta\mathbf{P} = \mathbf{B}_{\mathcal{N}\mathcal{M}^c} \Delta\boldsymbol{\theta}_{\mathcal{M}^c}$: it has a 1 at its index 1, and some possibly non-zero values at the indices that correspond to $N(\mathcal{M}^c)$, but has *zero values at all other indices*. Thus,

$$\|\Delta\mathbf{P}\|_0 \leq |N(\mathcal{M}^c)| + 1 \leq M + 1. \quad (5)$$

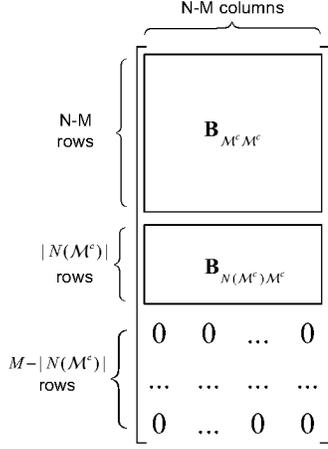
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From Theorem 1 and 2, we have closed the gap between the lower and upper bounds on the optimum of (3) for the case of $M \leq \kappa(\mathcal{G})$, and proved that the optimum in this case equals $M+1$. To further solve the case of $M > \kappa(\mathcal{G})$, we would like to improve the upper bound $|N(\mathcal{M}^c)|+1$.

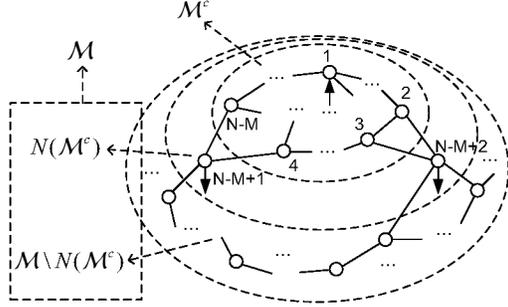
We observe that, by selecting a *subset* of the columns of $\mathbf{B}_{\mathcal{N}\mathcal{M}^c}$ to form a new submatrix $\mathbf{B}_{\mathcal{N}\mathcal{S}}, \mathcal{S} \subset \mathcal{M}^c$, and partitioning $\mathbf{B}_{\mathcal{N}\mathcal{S}}$ into three submatrices in the same way as $\mathbf{B}_{\mathcal{N}\mathcal{M}^c}$ is partitioned in Figure 1(a), it is possible that the resulting middle submatrix $\mathbf{B}_{N(\mathcal{S})\mathcal{S}}$ has *fewer* rows than $\mathbf{B}_{N(\mathcal{M}^c)\mathcal{M}^c}$, leading to even sparser unobservable attacks with sparsity $|N(\mathcal{S})|+1$. By further developing this idea based on the geometric insights behind the proof of Theorem 2, we provide a complete solution of (3) next.

C. Closing the gap between lower and upper bounds on the minimum sparsity of unobservable attacks

We start by providing a geometric interpretation of Theorem 2. As shown in Figure 1(a), if $\mathcal{M} \setminus N(\mathcal{M}^c) \neq \emptyset$, all the buses can be partitioned into three subsets $\mathcal{M}^c, N(\mathcal{M}^c)$ and $\mathcal{M} \setminus N(\mathcal{M}^c)$, corresponding to the row indices of the top, middle and bottom submatrices of $\mathbf{B}_{\mathcal{N}\mathcal{M}^c}$ respectively. Moreover, $N(\mathcal{M}^c)$ is a *vertex cut of \mathcal{G} that separates \mathcal{M}^c from $\mathcal{M} \setminus N(\mathcal{M}^c)$* . The $|N(\mathcal{M}^c)|+1$ -sparse attack $\Delta\mathbf{P}$ (cf. (5)) is formed by injecting power at *one* of the buses among \mathcal{M}^c (i.e., bus 1), and extracting power at and only at the buses in the vertex cut $N(\mathcal{M}^c)$, such that the phase angle changes at \mathcal{M} are all zero.



(a) Block representation of $\mathbf{B}_{\mathcal{N}, \mathcal{M}^c}$.



(b) A 3-sparse unobservable power injection attack.

Fig. 1. Sparse attacks with the voltage phase angles at the bus $1, 2, \dots, N-M$ affected.

An example that illustrates the above formation of an $|N(\mathcal{M}^c)|+1$ -sparse attack is depicted in Figure 1(b). The set $\mathcal{M}^c = \{1, 2, \dots, N-M\}$ interacts with the set $\mathcal{M} \setminus N(\mathcal{M}^c)$ via only *two* buses — $N(\mathcal{M}^c) = \{N-M+1, N-M+2\}$. As a result, there exists a 3-sparse attack that injects/extracts power at bus $1, N-M+1$ and $N-M+2$, such that the phase angle changes at \mathcal{M} are all zero.

More generally, for all $\mathbf{B}_{\mathcal{N}, \mathcal{S}}$, $\mathcal{S} \subseteq \mathcal{M}^c$, the above geometric interpretation applies as well: Given such \mathcal{S} , $N(\mathcal{S})$ is a *vertex cut that separates \mathcal{S} from $\mathcal{M} \setminus N(\mathcal{S})$* . Therefore, finding the minimum $|N(\mathcal{S})|$ is similar to finding a *minimum vertex cut of a graph, but with a key restriction of $\mathcal{S} \subseteq \mathcal{M}^c$ that represents the unobservability condition (1)*. Note that the minimum vertex cut of \mathcal{G} may not be a legitimate $N(\mathcal{S}), \mathcal{S} \subseteq \mathcal{M}^c$ for the following reasons: It can happen that the minimum vertex cut *disconnects* the remaining part of \mathcal{M} such that part of \mathcal{M} is in \mathcal{S} , violating $\mathcal{S} \subseteq \mathcal{M}^c$. To ensure the restriction $\mathcal{S} \subseteq \mathcal{M}^c$, we construct a variation of the original grid \mathcal{G} :

Definition 4: Given a set of buses $\mathcal{M} \subseteq \mathcal{N}$, $\mathcal{G}^{\mathcal{M}}$ is defined to be the following augmented graph based on \mathcal{G} : $\mathcal{G}^{\mathcal{M}}$ has the same set of buses as \mathcal{G} , and in addition to all the existing edges of \mathcal{G} , an edge is added between every pair of buses in \mathcal{M} with its weight an independent continuous random variable strictly bounded away from zero from below.

We then have the following lemma that ensures the legit-

imacy of a vertex cut as $N(\mathcal{S})$ for some $\mathcal{S} \subseteq \mathcal{M}^c$:

Lemma 1: For any (including the minimum) vertex cut of the augmented graph $\mathcal{G}^{\mathcal{M}}$, there is no pair of buses in \mathcal{M} that is disconnected by this cut.

From Lemma 1, any vertex cut (corresponding to $N(\mathcal{S})$ for some $\mathcal{S} \subseteq \mathcal{M}^c$) of the augmented graph $\mathcal{G}^{\mathcal{M}}$ partitions $\mathcal{G}^{\mathcal{M}}$ into three subgraphs:

- 1) $\mathcal{S} (\subseteq \mathcal{M}^c)$ which consists of all the buses that are disconnected from \mathcal{M} after removing the cut set $N(\mathcal{S})$.
- 2) $N(\mathcal{S})$ which is the vertex cut set itself.
- 3) $\mathcal{N} \setminus N[\mathcal{S}]$ which contains (not necessarily exclusively) the remaining buses in \mathcal{M} after removing this cut set.

By the same arguments as in the constructive proof of Theorem 2, an $|N(\mathcal{S})|+1$ -sparse unobservable attack can be formed by changing the power injections at *one* of the buses in \mathcal{S} as well as at all the buses in the cut set $N(\mathcal{S})$.

Now, note that an attack is unobservable by \mathcal{M} in \mathcal{G} if and only if it is unobservable by \mathcal{M} in $\mathcal{G}^{\mathcal{M}}$. We then have the following theorem that closes the gap between the lower and upper bounds of (3):

Theorem 3: For a connected power grid $\mathcal{G} = \{\mathcal{N}, \mathcal{L}, w\}$, assume that the line reactances x_l ($l \in \mathcal{L}$) are independent continuous random variables strictly bounded away from zero from below. Given any $\mathcal{M} \subseteq \mathcal{N}$, $|\mathcal{M}| = M$ denoting the set of buses with PMUs, the minimum sparsity of unobservable attacks, i.e., the global optimum of (3), equals $\min(\kappa(\mathcal{G}^{\mathcal{M}}), M) + 1$ with probability one.

Finally, we note that finding the minimum vertex cut of a graph is computationally efficient. Thus, Theorem 3 not only shows that the sparsest attack (3) can be found in polynomial time with probability one, but also crystalizes the geometric interpretation of the optimal solutions. In particular, the constructive solution based on the minimum vertex cut of the augmented graph $\mathcal{G}^{\mathcal{M}}$ is guaranteed to be optimal for solving (3). Next, we will show that this geometric insight leads to a natural characterization of the potential impacts of unobservable attacks.

D. Potential impacts of unobservable attacks

As discussed in the last section, a vertex cut of $\mathcal{G}^{\mathcal{M}}$, denoted by $N(\mathcal{S})$ for some $\mathcal{S} \subseteq \mathcal{M}^c$, partitions $\mathcal{G}^{\mathcal{M}}$ into \mathcal{S} , $N(\mathcal{S})$ and $\mathcal{N} \setminus N[\mathcal{S}]$. We now show that, *given any power injection changes in \mathcal{S}* , appropriate power injection changes in $N(\mathcal{S})$ exist such that the voltage phase angles in $\mathcal{N} \setminus \mathcal{S} (\supseteq \mathcal{M})$ do not change. We illustrate this for the case of $\mathcal{S} = \mathcal{M}^c$, and generalizations to $\mathcal{S} \subseteq \mathcal{M}^c$ follow immediately.

As in the proof of Theorem 2 (cf. Figure 1(a)), for any power injection changes in $\mathcal{S} = \mathcal{M}^c$, denoted by $\Delta \mathbf{P}_{\mathcal{M}^c}$, we let $\Delta \boldsymbol{\theta}_{\mathcal{M}^c} = \mathbf{B}_{\mathcal{M}^c, \mathcal{M}^c}^{-1} \Delta \mathbf{P}_{\mathcal{M}^c}$. Then, we let $\Delta \mathbf{P}_{N(\mathcal{M}^c)} = \mathbf{B}_{N(\mathcal{M}^c), \mathcal{M}^c} \Delta \boldsymbol{\theta}_{\mathcal{M}^c}$. Finally, we let $\Delta \mathbf{P}_{\mathcal{M} \setminus N(\mathcal{M}^c)} = \mathbf{0}$ and $\Delta \boldsymbol{\theta}_{\mathcal{M}} = \mathbf{0}$. From the block representation of $\mathbf{B}_{\mathcal{N}, \mathcal{M}^c}$ as in Figure 1(a), the above constructions satisfy

- $\Delta \mathbf{P} = \mathbf{B} \Delta \boldsymbol{\theta}$, and
- The voltage phase angles in $\mathcal{N} \setminus \mathcal{S} = \mathcal{M}$ do not change.

Consequently, we have the following corollary:

Corollary 1: As long as an attacker takes control of the power injections of all the buses in a vertex cut $N(\mathcal{S})$, it can

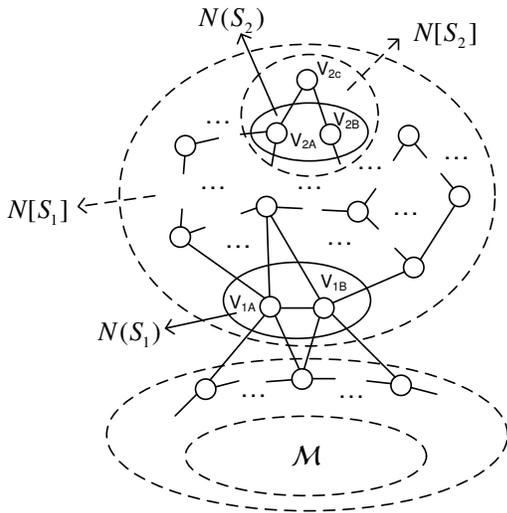


Fig. 2. An illustration of two minimum vertex cuts with the same size but different potential impacts.

always *cancel out* the effects of anything that happens within S on the measurements by the set of PMUs \mathcal{M} ($\subseteq \mathcal{N} \setminus S$).

From Corollary 1, by taking control of the buses in a cut $N(S)$, an attacker is able to hide from the system operator a power injection attack with a zero norm as large as

$$|N[S]| = |N(S)| + |S| (\gg |N(S)| + 1). \quad (6)$$

Accordingly, we define the potential impact of unobservable attacks as follows:

Definition 5: For any vertex cut $N(S)$ for some $S \subseteq \mathcal{M}^c$, the potential impact of unobservable attacks by controlling power injections at $N(S)$ is $|N[S]|$.

Employing Definition 5, we can *differentiate* the potential impacts of multiple sparsest unobservable attacks *with the same sparsity*. In particular, multiple minimum vertex cuts can exist for the same augmented graph $\mathcal{G}^{\mathcal{M}}$. Then, each of these cuts leads to a different sparsest unobservable attack of the same size (constructed by controlling the buses in this cut as well as one other bus disconnected from \mathcal{M} by it). However, different cuts may disconnect different portions of the network from \mathcal{M} , leading to vastly different potential impacts of unobservable attacks. An illustration is depicted in Figure 2. In this example, two vertex cuts both of size two, $N(S_1) = \{V_{1A}, V_{1B}\}$ and $N(S_2) = \{V_{2A}, V_{2B}\}$, are noted as enclosed by solid ovals. Accordingly, both cuts enable 3-sparse unobservable attacks. However, their potential impacts are significantly different. Cut $N(S_2)$ only disconnects one other bus, namely $S_2 = \{V_{2C}\}$ from the set of PMUs \mathcal{M} , and hence its potential impact equals $|N[S_2]| = 3$; in comparison, cut $N(S_1)$ disconnects all the vertices above $N(S_1)$ from \mathcal{M} , and hence its potential impact equals $|N[S_1]| \gg 3$. With this definition of potential impact, it is then natural for an attacker to *seek the sparsest unobservable attack with the greatest potential impact*.

Finally, we conclude this section by noting the following fact: the minimum sparsity and the potential impacts of

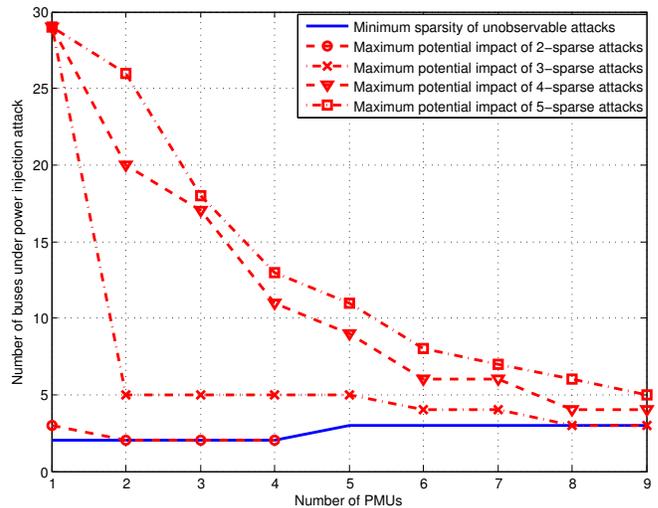


Fig. 3. Minimum sparsity of unobservable attacks and maximum potential impact of 2, 3, 4, 5-sparse attacks as functions of M ; IEEE 30-bus system.

unobservable attacks are fully determined with probability one by the *network topology and the locations of the PMUs*.

IV. NUMERICAL EVALUATION

In this section, we evaluate the sparsest unobservable attacks and their potential impacts when the system operator deploys PMUs at optimized locations. We have seen in Section III that the minimum sparsity and potential impacts of unobservable attacks are determined fully by the network topology and the PMU placement. Unlike network states and network parameters which can vary over short and medium time scales, the transmission network topology (or the set of possible topologies) typically stays the same over long time scales. The above motivates the system operator to optimize the PMU placement according to the network topology. For the best performance in countering power injection attacks, the system operator wants to *raise the minimum sparsity of unobservable attacks, as well as mitigate the maximum potential impact of unobservable attacks*. The geometric interpretations of the sparsest unobservable attacks and their potential impacts allow us to develop an efficient PMU placement algorithm for the system operator to pursue both objectives. The details of the algorithm are omitted here due to space limitations, and can be found in [10].

We evaluate our results in IEEE 30-bus, IEEE 57-bus, IEEE 118-bus, IEEE 300-bus, Polish 2383-bus, Polish 2737-bus, and Polish 3012-bus systems. The evaluation is performed based on the software toolbox MATPOWER [11]. In each of these systems, we generate a set of PMUs based on the developed placement algorithm, with the number of PMUs increasing from one until all attacks become observable. For all numbers of PMUs, the minimum sparsity of unobservable attacks as well as the maximum potential impact among the sparsest unobservable attacks are found.

Specifically, the minimum sparsity of unobservable attacks and the maximum potential impact among these sparsest attacks both as functions of the number of PMUs M are plotted

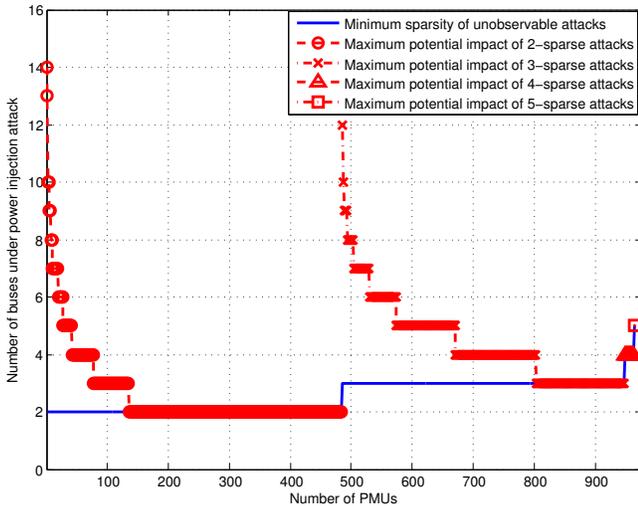


Fig. 4. Minimum sparsity of unobservable attacks and the maximum potential impact of the sparsest attacks as functions of M ; Polish 3012-bus system.

for the IEEE 30-bus power system and the Polish 3012-bus system in Figure 3 and 4 respectively. In addition, for the IEEE 30-bus system, the maximum potential impacts among all 2-sparse, 3-sparse, 4-sparse and 5-sparse unobservable attacks for the entire range of M are plotted. (Note that the minimum sparsity of unobservable attacks does not exceed 3 for all M .)

We make the following observations which appear in all seven of the evaluated systems:

- In *all* seven systems, all the attacks become observable with *less than a third* of the buses installed with PMUs (assuming the second PMU model). The average percentage of the number of PMUs needed to have full network observability equals 31.1%. This number resembles a well-known estimate of such percentage to be one third [12].
- The topologies of the tested power systems tend to allow sparse power injection attacks. In other words, the vertex connectivity of these power networks is often small. Furthermore, there are often *many ties* when finding unobservable attacks with the minimum sparsity: this is why even after adding a lot more PMUs into the network, with each addition eliminating the previous sparsest attack, the minimum sparsity can still remain the same.
- While there are many ties of unobservable attacks with the same sparsity, the potential impacts among them can vary significantly. Moreover, as more PMUs are added, the maximum potential impact among all the sparsest unobservable attacks drops quickly until it reaches the minimum sparsity.

V. CONCLUSION

We have studied cyber-physical attacks that alter power generation and loads in power networks while remaining unobservable under the surveillance of system operators

using PMUs. We have provided an explicit solution to the open problem of finding the sparsest unobservable attacks; the minimum sparsity among all unobservable attacks equals $\min(\kappa(\mathcal{G}^M), M) + 1$. In deriving this minimum sparsity, a lower bound on it based on the vertex connectivity of the network was shown to hold with probability one. We have then provided a constructive upper bound that successfully closes the gap to the lower bound. This constructive upper bound enables us to find all the sparsest unobservable attacks in polynomial time by finding the minimum vertex cuts of an augmented graph \mathcal{G}^M . As a result, $\min(\kappa(\mathcal{G}^M), M) + 1$ is a fundamental limit of this minimum sparsity that is not only explicitly attainable, but also unbeatable by all possible unobservable attacks. We have further shown that the geometric interpretation of the unobservable attacks allows a natural characterization of their potential impacts. With optimized PMU deployment, we have evaluated the sparsest unobservable attacks and their potential impacts in IEEE 30, 57, 118, 300-bus systems and Polish 2383, 2737, 3012-bus systems. Finally, while this work has studied a static system model and power injection attacks, extension to dynamic systems, measurements and power injection attacks remains an interesting future direction, for which we expect that similar insights on fundamental limits will apply.

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