ESE 355 VLSI System Design
Spring Semester 2005

Catalog Data: ESE 355 VLSI System Design. Credits 4. Introduces techniques and tools for scalable VLSI design and analysis. Emphasis is on physical design and on performance analysis. Includes extensive lab experiments and hands-on usage of CAD tools.

Text Books:

Instructor: Dr. Alex Doboli, 261 Light Engineering Bld., 632-1611, adoboli@ece.sunysb.edu

Goals: Upon completion of this course, the students will be able to develop, layout and simulate fabricatable designs of VLSI building-block modules, such as registers and arithmetic units, and medium-scale VLSI chips.

Prerequisites by Topic:
1. Logic Design - combinational and sequential.
2. Computer organization and microprocessors.
3. Electronics - MOS transistor theory.
4. Basic Network Analysis

Topics:
1. CMOS Device Characteristics and processing
2. Basic MOS Transistor Theory
3. Switch Level Simulation, Timing Analysis
4. Performance Estimation, resistance, capacitance, power, fall and rise times.
5. Transistor Sizing
6. Scalable Designs and Design Rules
7. CMOS logic families and circuit techniques.
8. Latch-Up and solutions.
9. Clocks and multiphase clocking.
10. VLSI Building Blocks - Inverters, Gates, Multiplexers, Registers, Arithmetic and Logic Units.
11. Hierarchical, modular and bit-sliced design techniques.
13. Area-Time-Power Tradeoffs in VLSI Design
15. Other VLSI Design Issues

Laboratory:
Unix workstations in Room 114, Old Engr Building will be used for homeworks & miniproject.

Assignment Schedule and Grading

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<td>Homework 1</td>
<td>Jan 21 2005</td>
<td>Feb 16 2005</td>
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<td>Miniproject</td>
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