ESE 345: Computer Architecture

Fall 2002

Description: This course focuses on the fundamental techniques of designing and evaluating modern computer architectures and tradeoffs present at the hardware/software boundary. The emphasis is on instruction set design, processor design, memory and input/output. Students will undertake a design project using modern CAD tools.

Prerequisites: ESE 380 (ESE 318 and CSE 220 for CSE majors). 3 credits

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Lecture: MW 7:00-8:24 PM, Physics 116

Text: David A. Patterson and John L. Hennessy
“Computer Organization & Design
The Hardware/Software Interface” 2nd ed., 1998

Project Deadline: Dec. 11, 2002

Course Grading: Homeworks: 15%
Exams: (two in-class midterms): 65%
Project: 20%

If you have a physical, psychological, medical or learning disability that may impact on your ability to carry out assigned course work, I would urge that you contact the staff in the Disabled Student Services office (DSS), room 133 Humanities, 632-6748/TDD. DSS will review your concerns and determine, with you, what accommodations are necessary and appropriate. All information and documentation of disability is confidential.